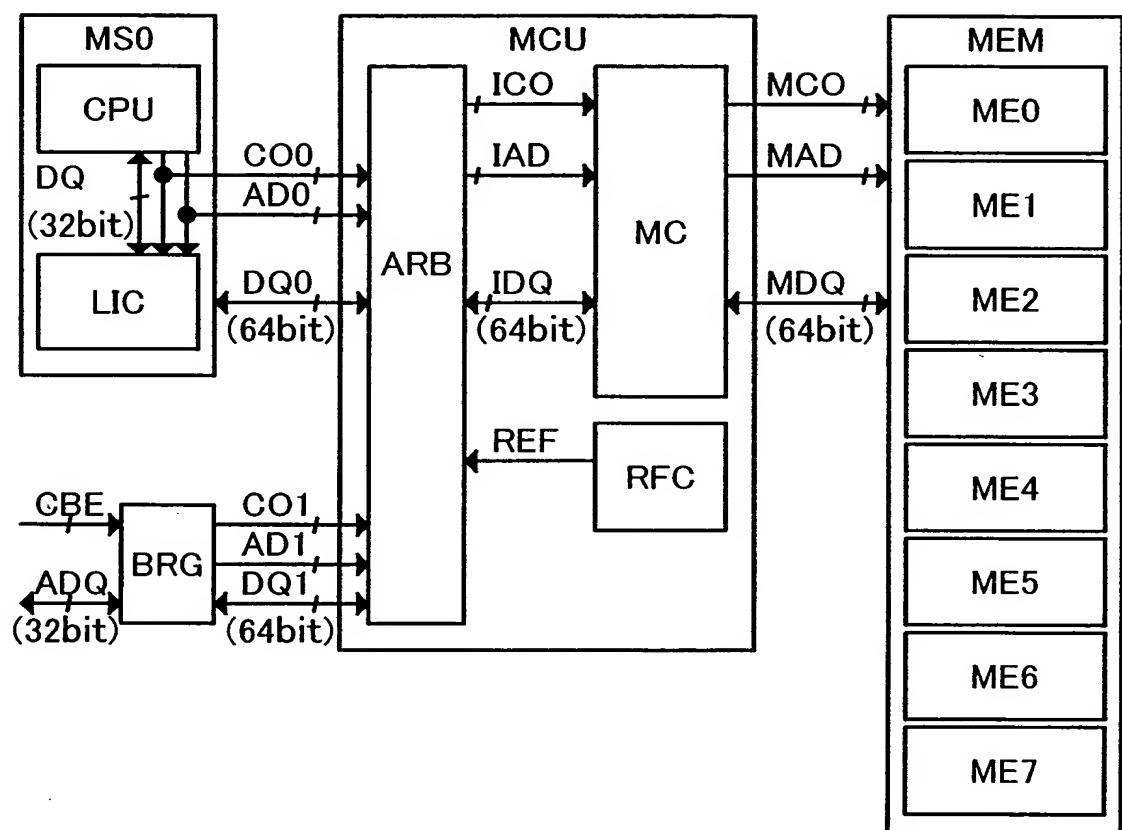
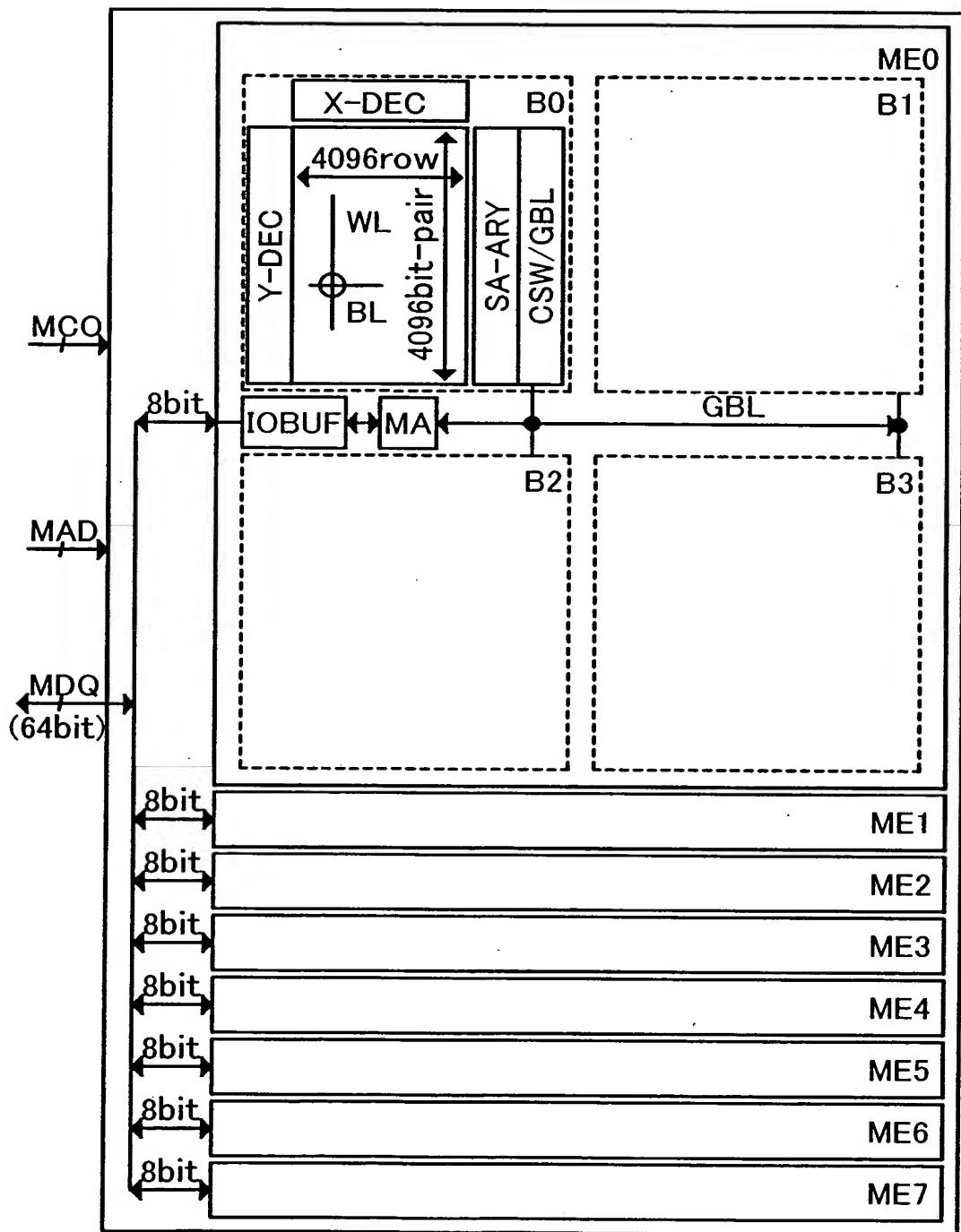


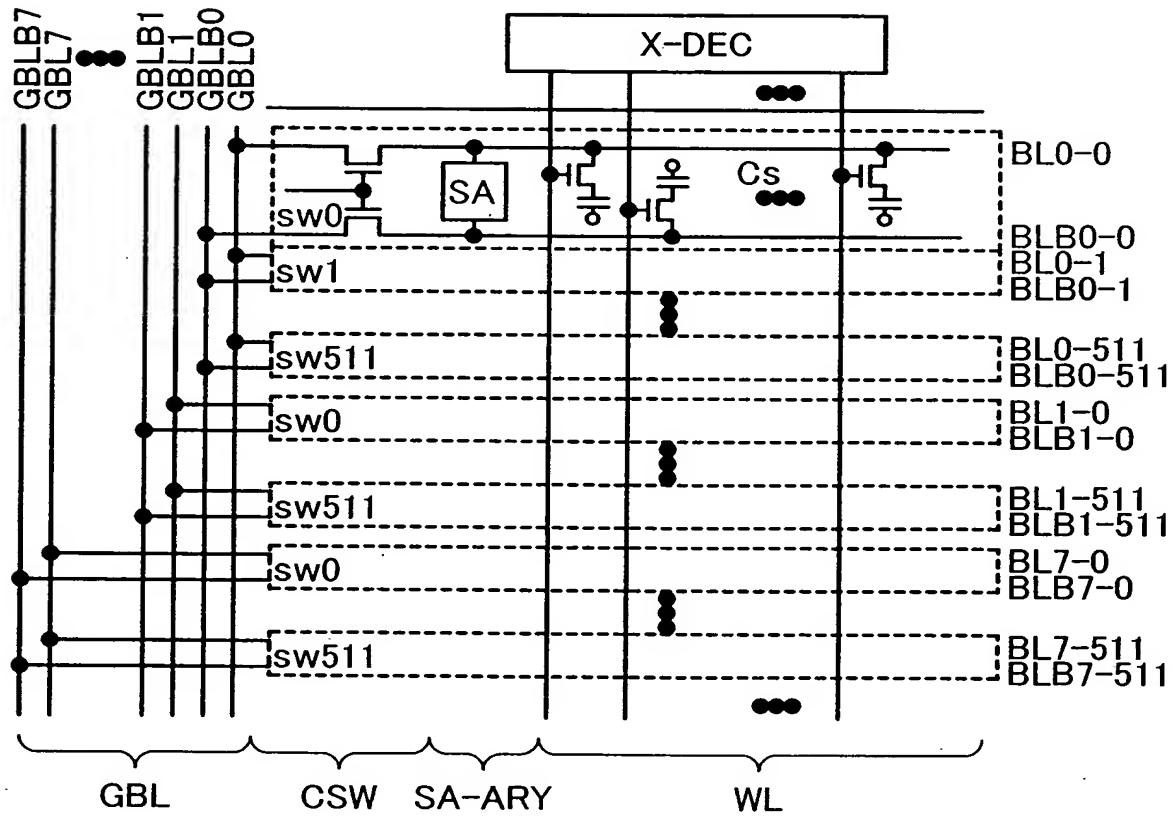
# FIG. 1



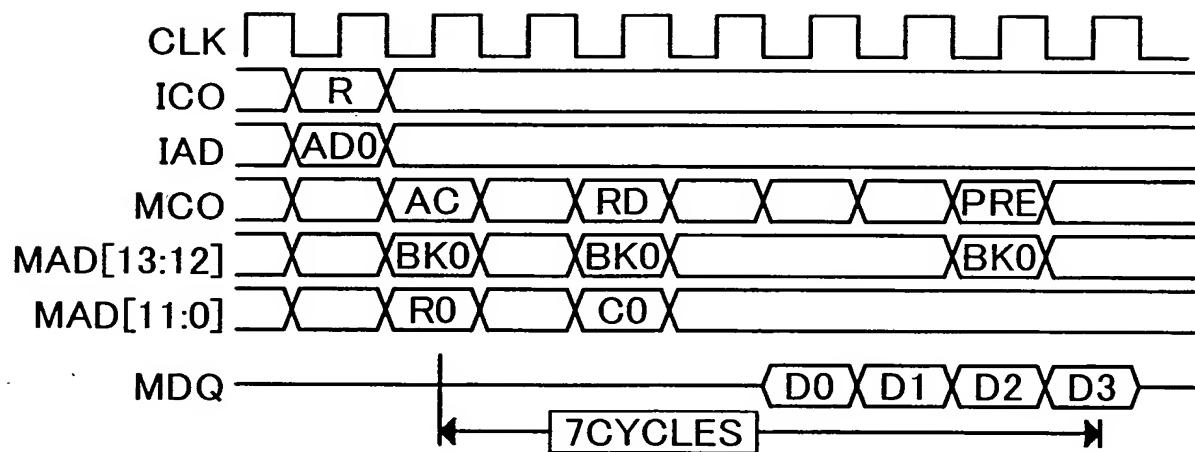
# FIG. 2



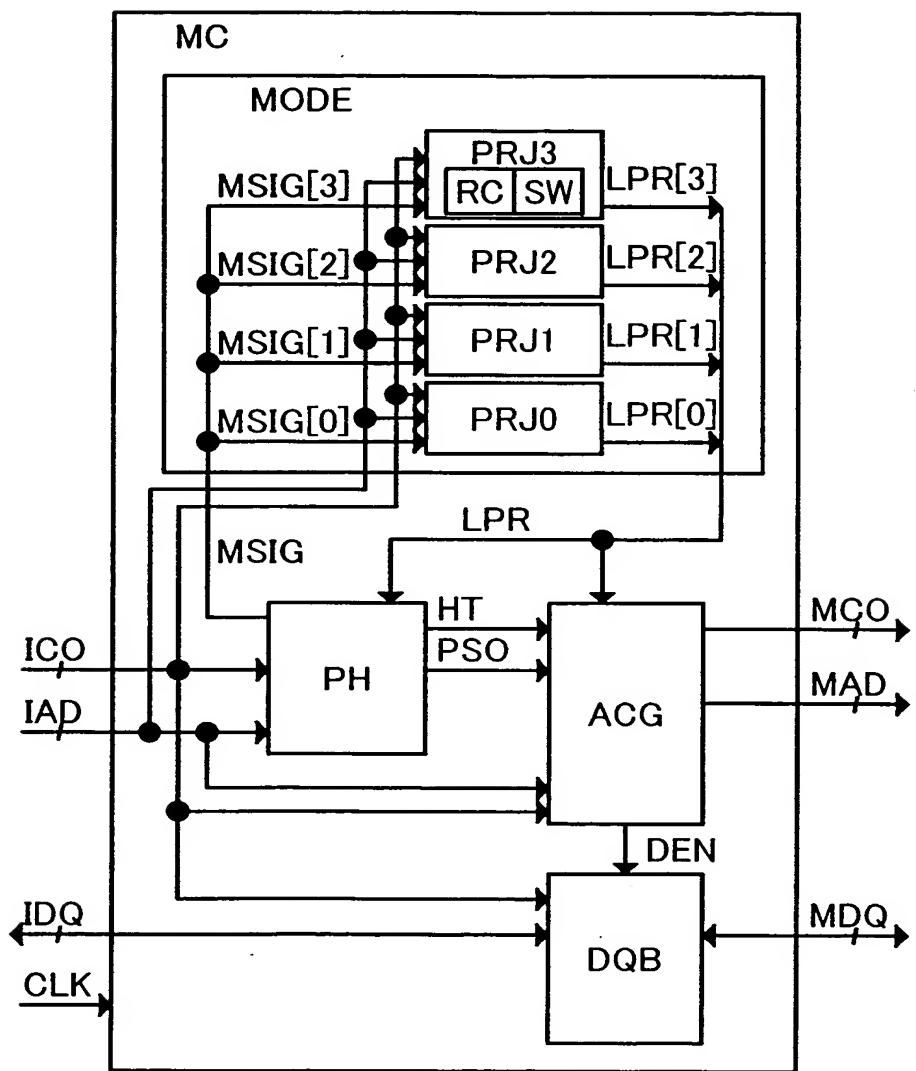
# FIG. 3



# FIG. 4



# FIG. 5

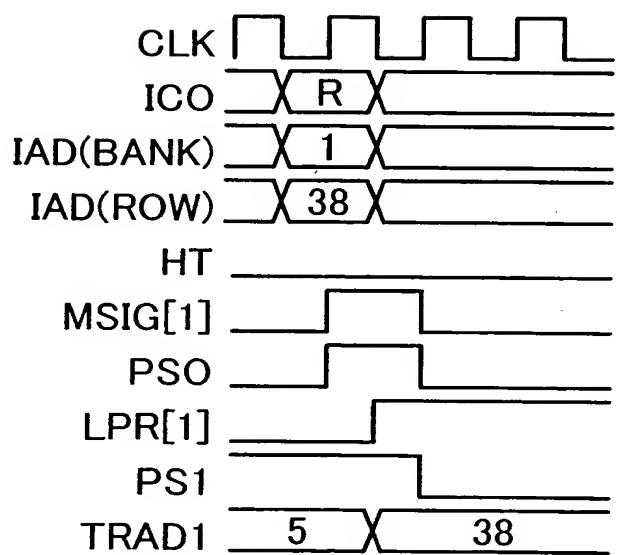


# FIG. 6

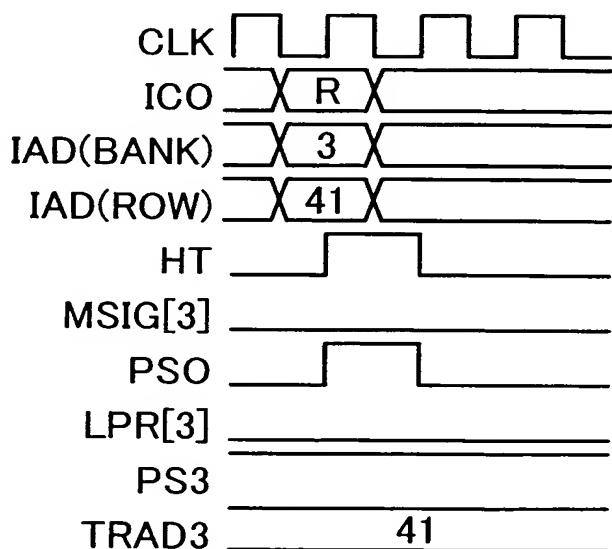
BANK	PS	TRAD
0	L	2
1	H	5
2	H	10
3	H	41

L: LOW H: HIGH

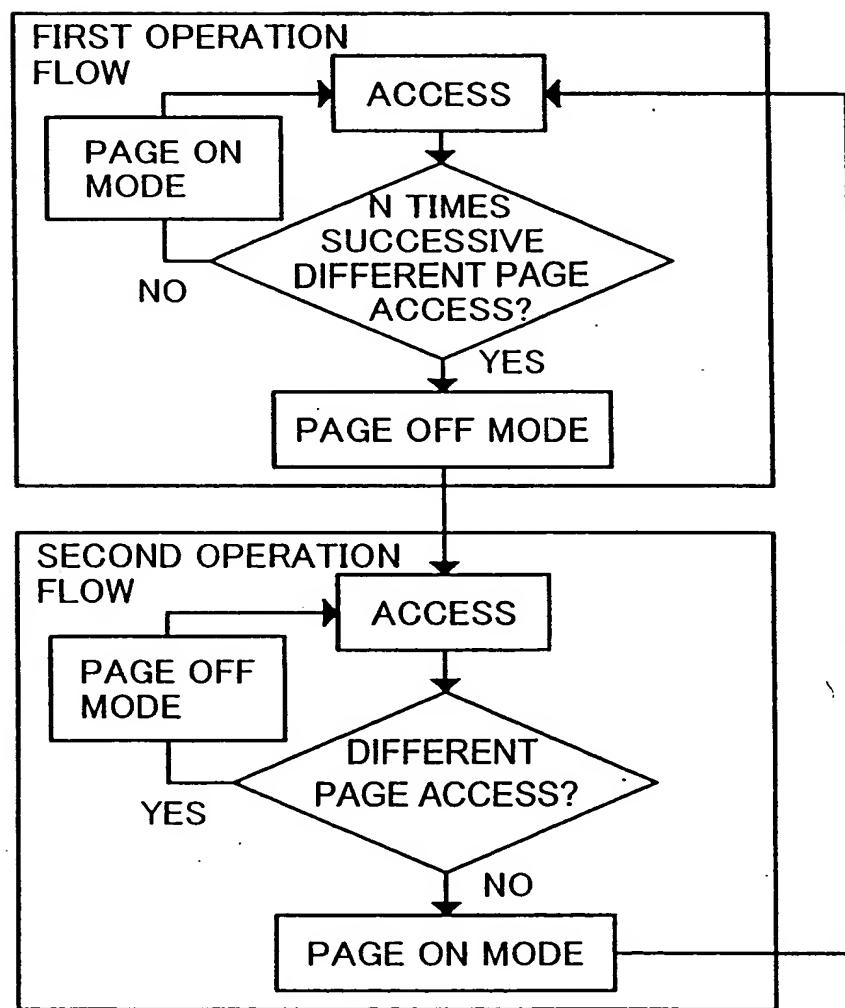
# FIG. 7A



# FIG. 7B



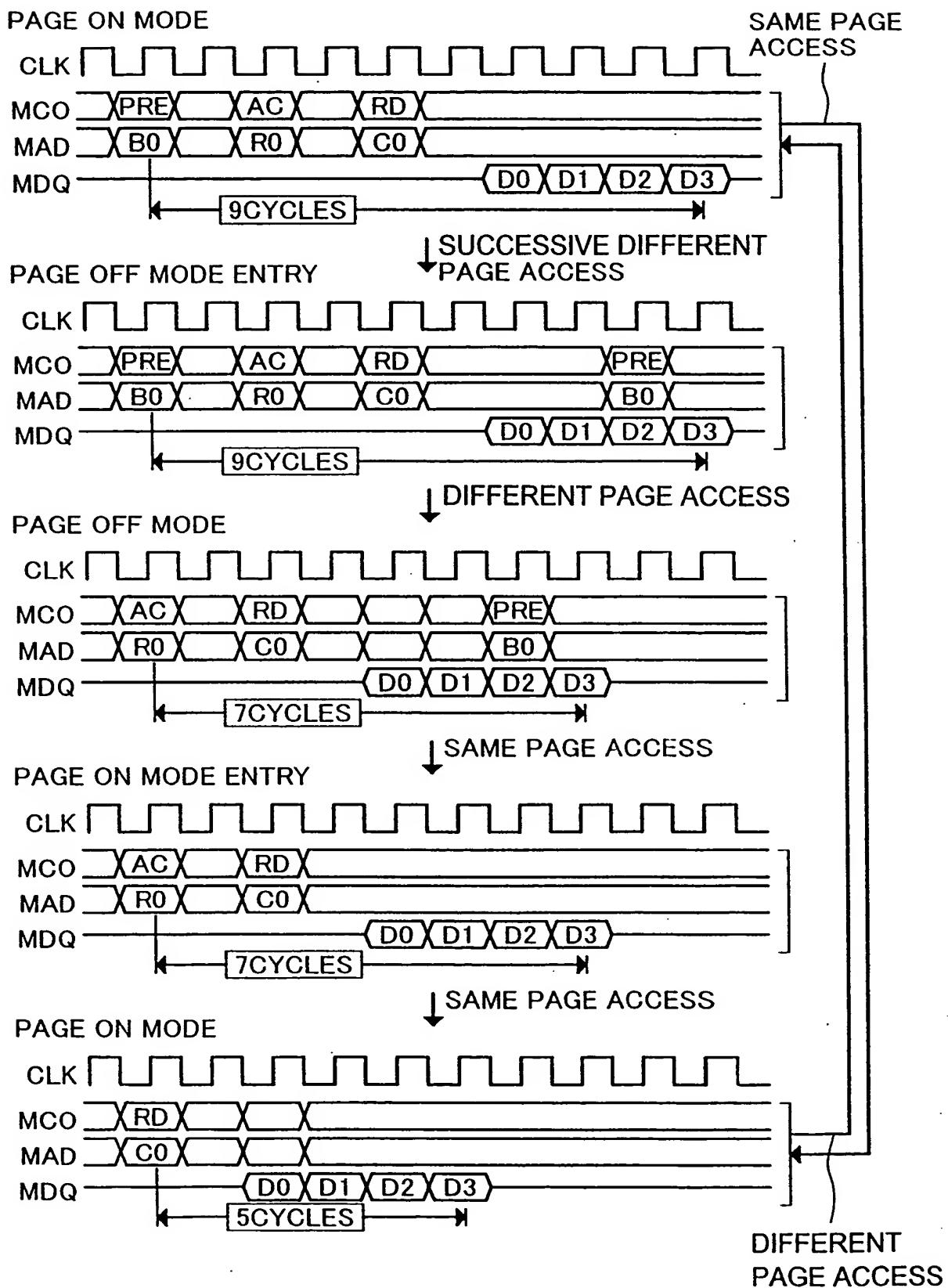
**FIG. 8**



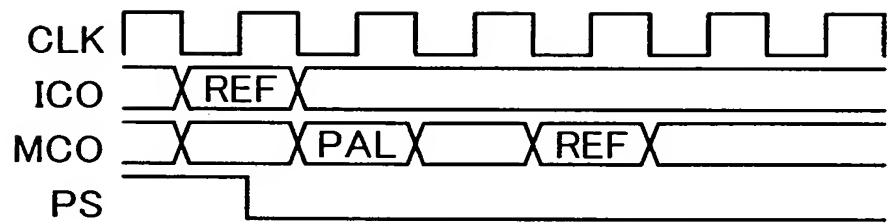
# FIG. 9

MODE LPR		PAGE ON MODE L				PAGE OFF MODE H			
PAGE HT		SAME PAGE H		DIFFERENT PAGE L		SAME PAGE H		DIFFERENT PAGE L	
PS		H	L	H	L	H	L	H	L
COMMAND SEQUENCE		RD	AC ↓ RD	PRE ↓ AC ↓ RD	AC ↓ RD	RD ↓ PRE	AC ↓ RD ↓ PRE	PRE ↓ AC ↓ RD ↓ PRE	AC ↓ RD ↓ PRE
READ LATENCY		5	7	9	7	5	7	9	7
WRITE LATENCY		3	5	7	5	3	5	7	5
		(A)	(B)	(C)	(D)	(E)	(F)	(G)	(H)

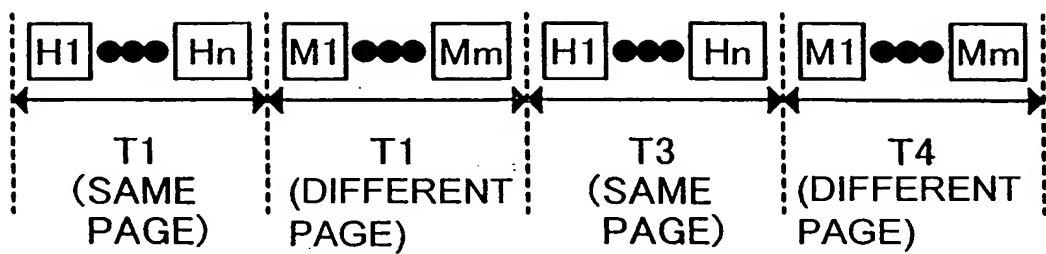
# FIG. 10



# FIG. 11

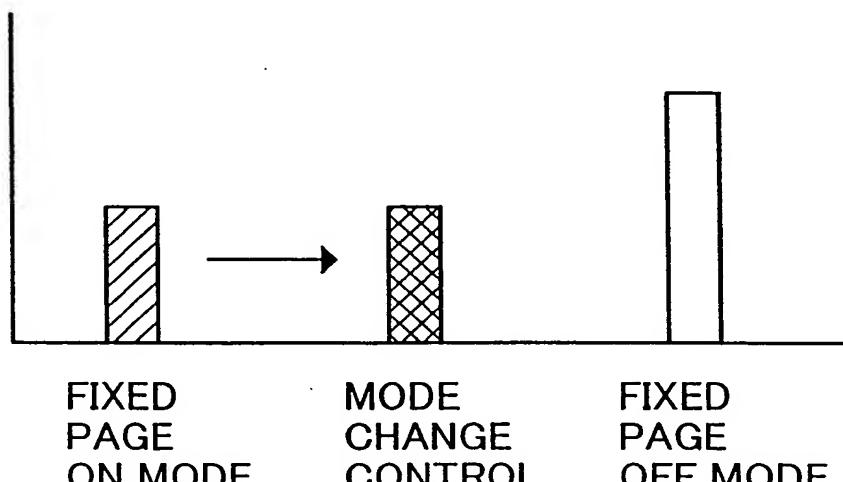


## FIG. 12A



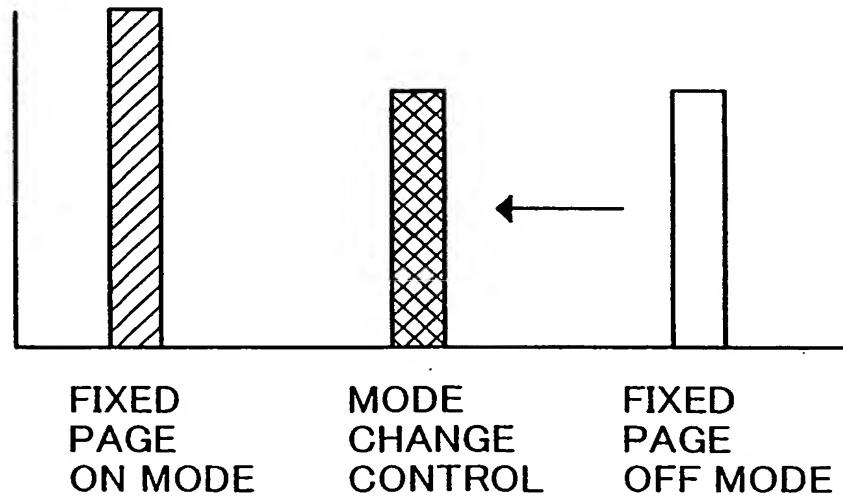
## FIG. 12B

LATENCY

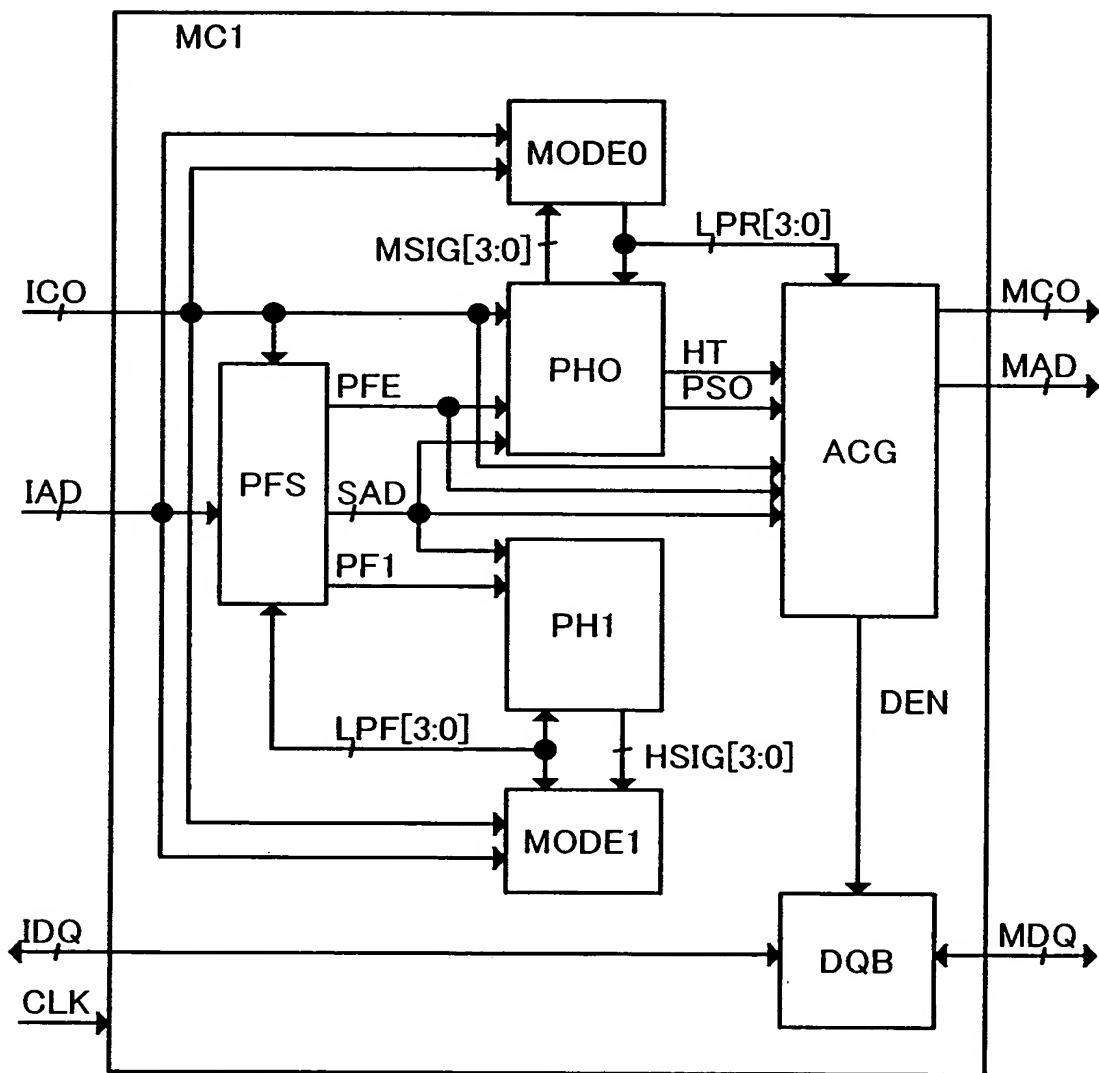


## FIG. 12C

LATENCY



**FIG. 13**



# FIG. 14

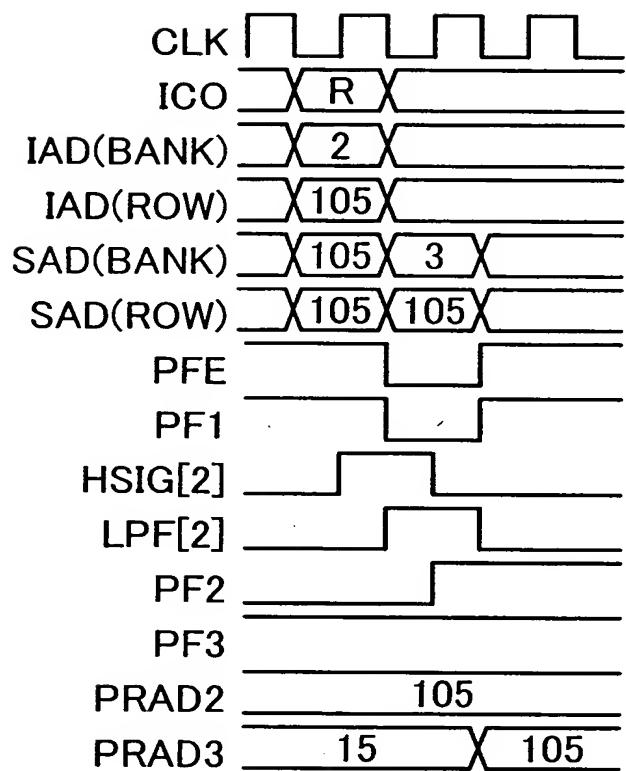
BANK	PRAD
0	8
1	6
2	105
3	15

# FIG. 15

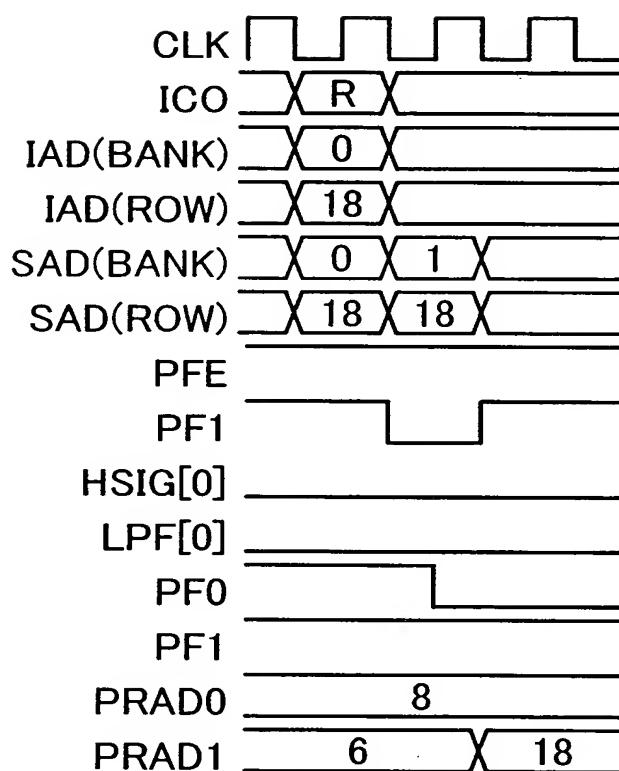
BANK	PF
0	H
1	L
2	L
3	H

L: LOW H: HIGH

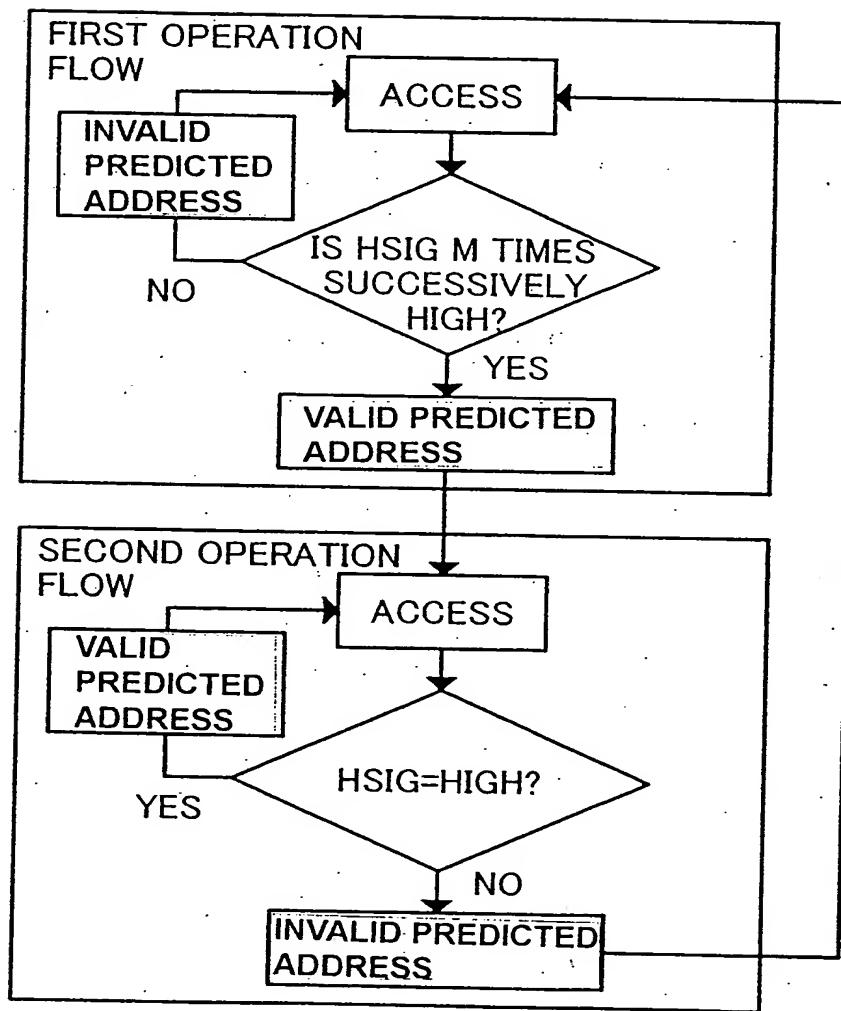
**FIG. 16A**



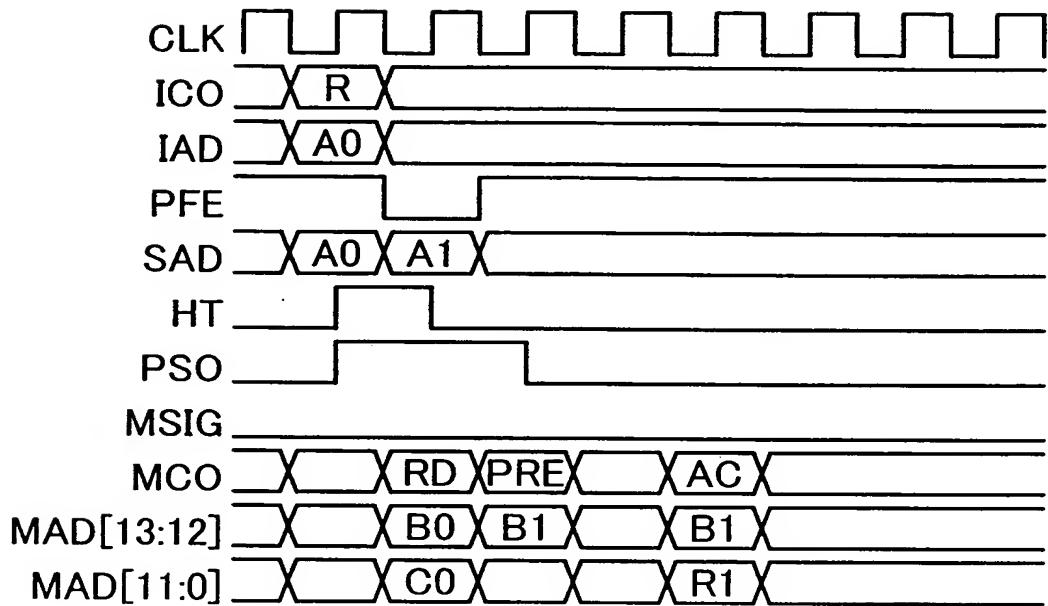
**FIG. 16B**



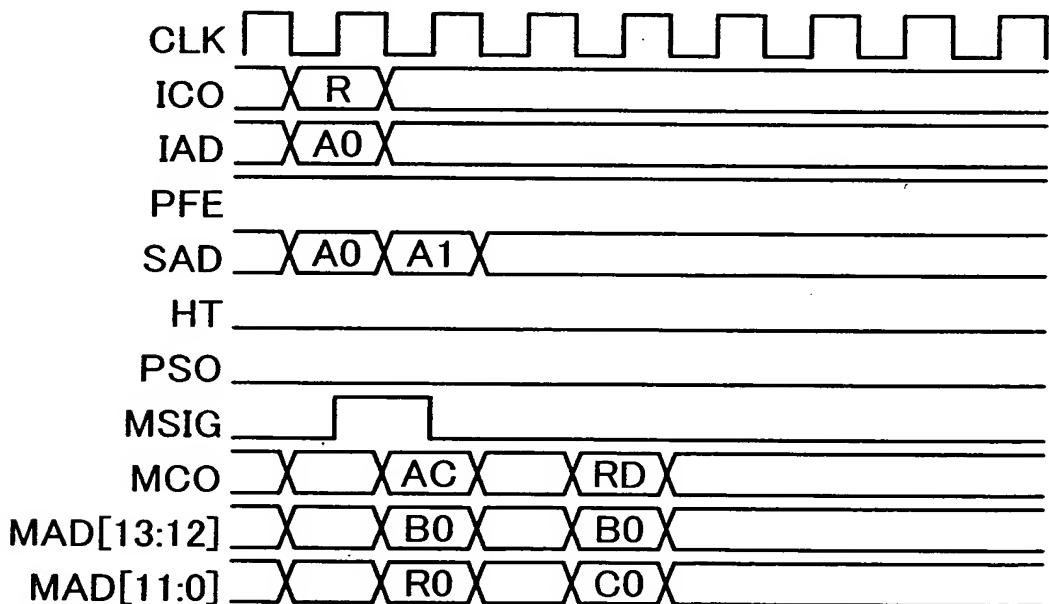
# FIG. 17



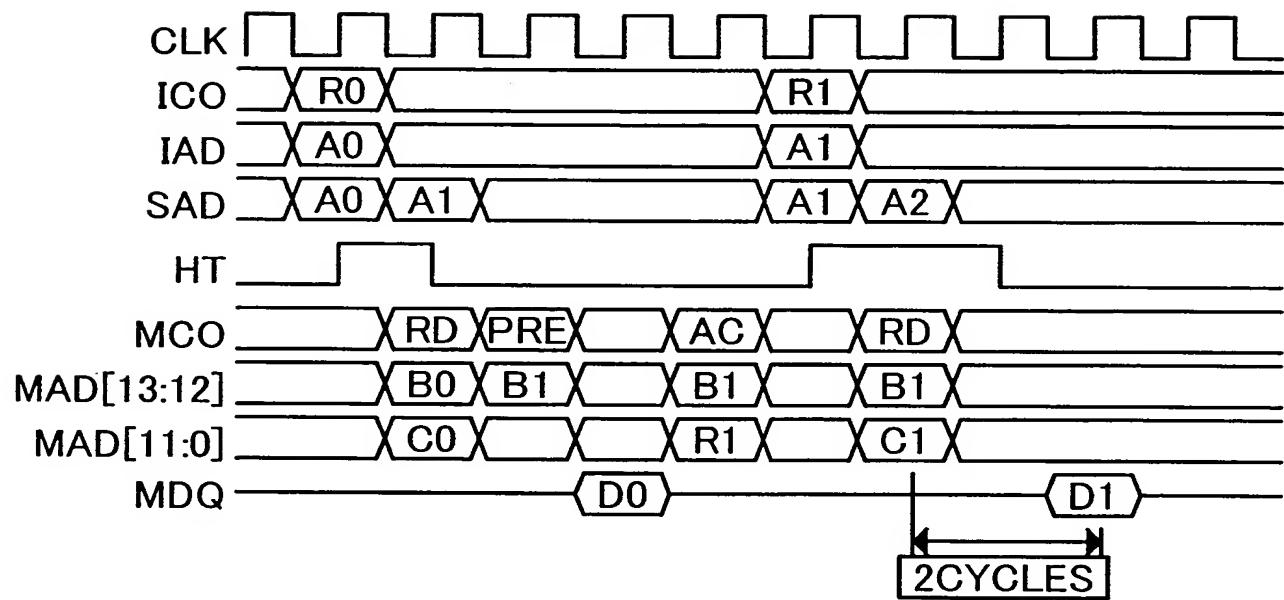
## FIG. 18A



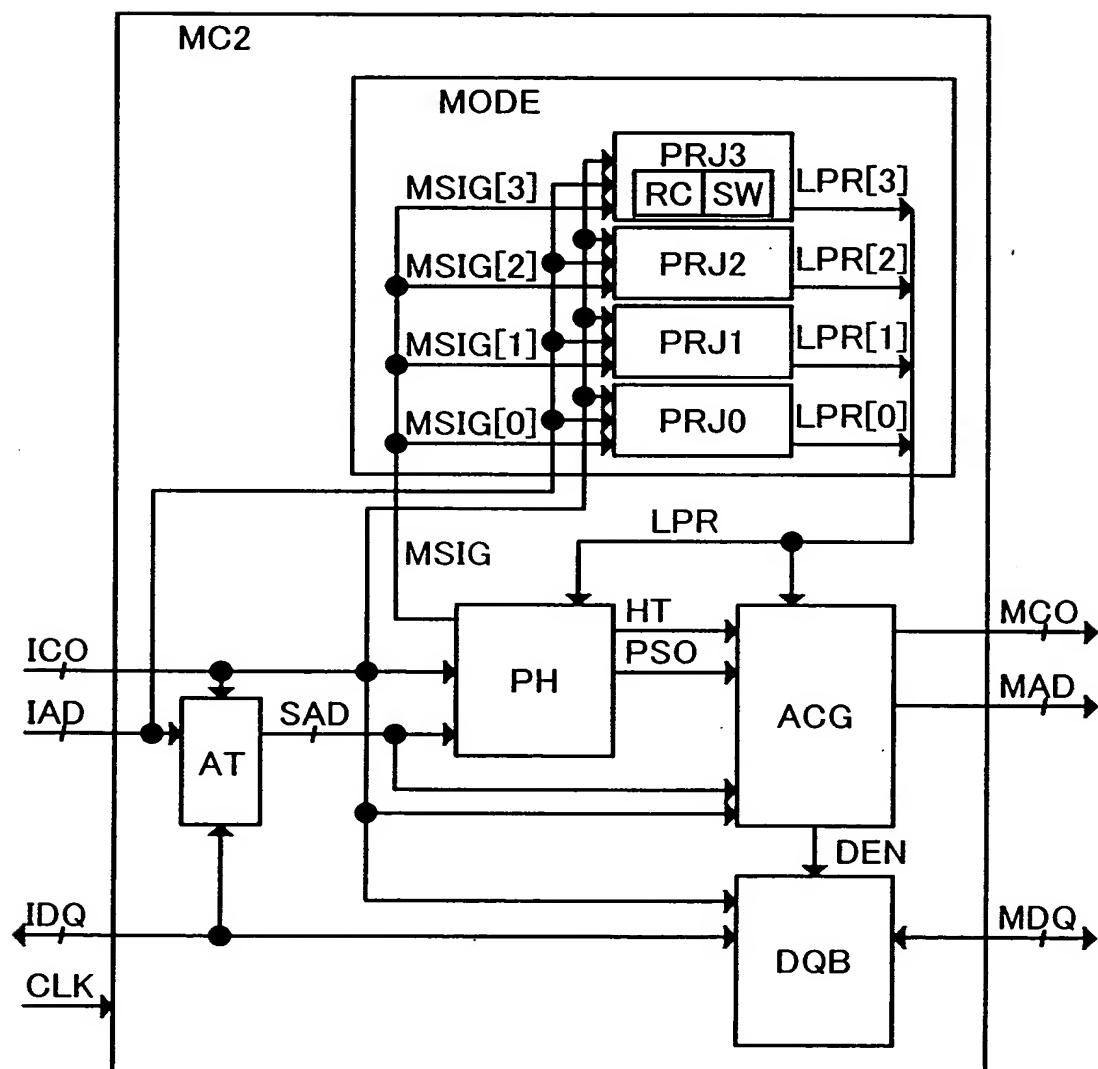
## FIG. 18B



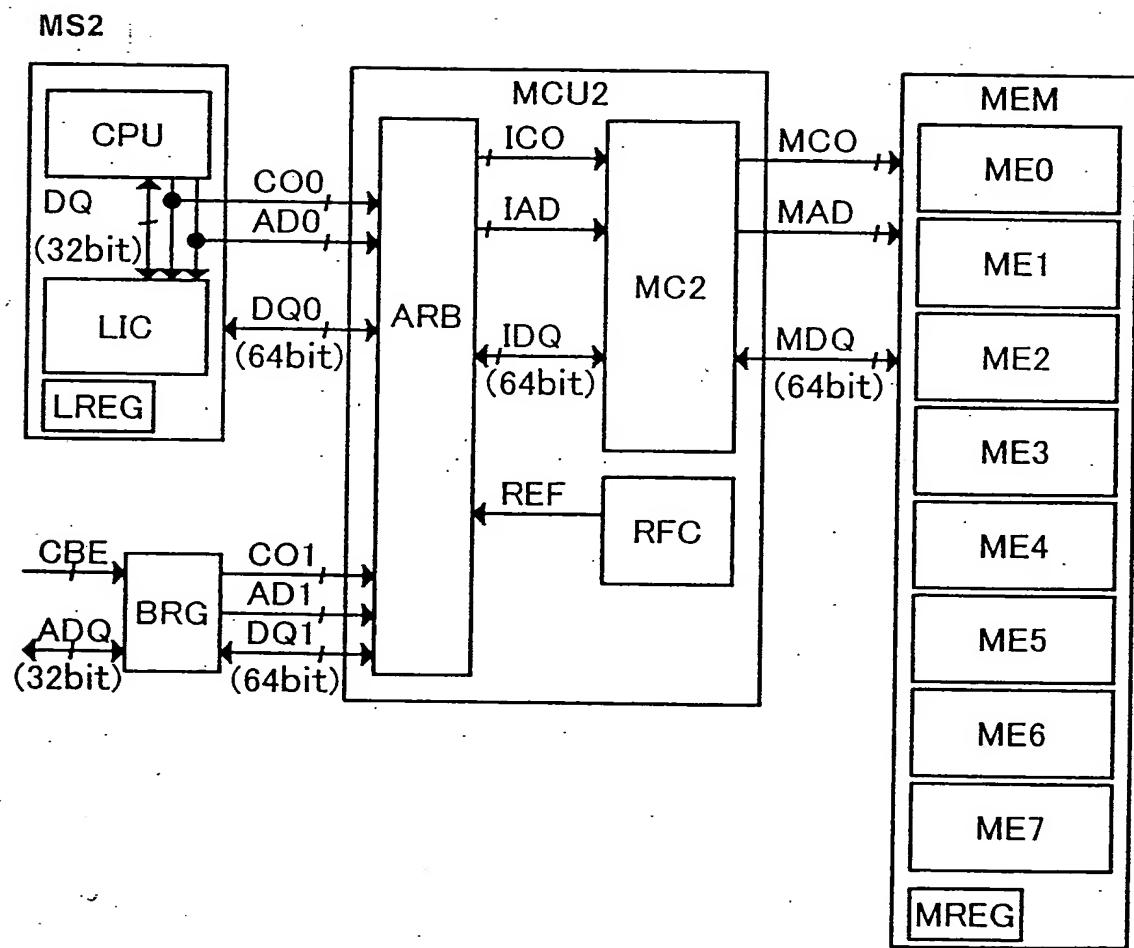
# FIG. 19



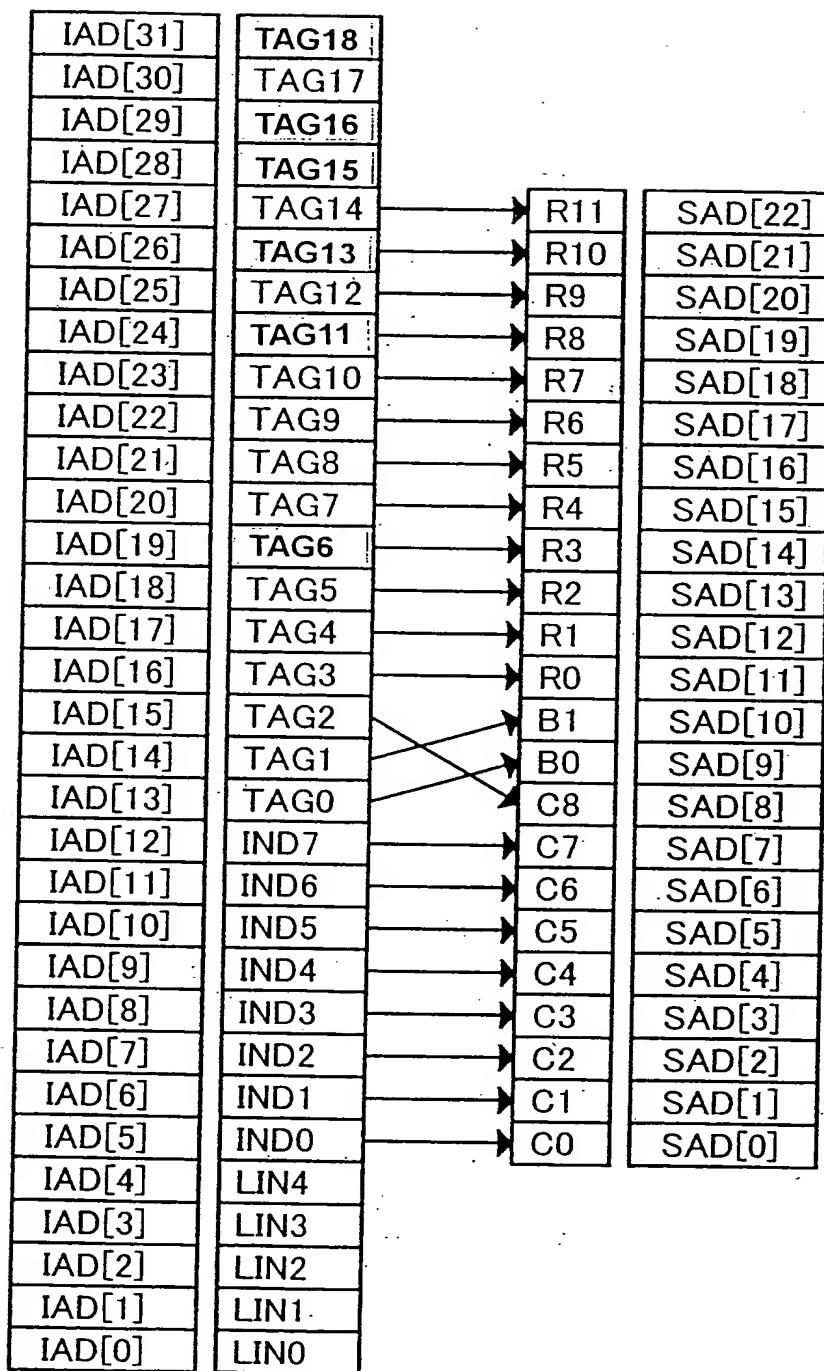
# FIG. 20



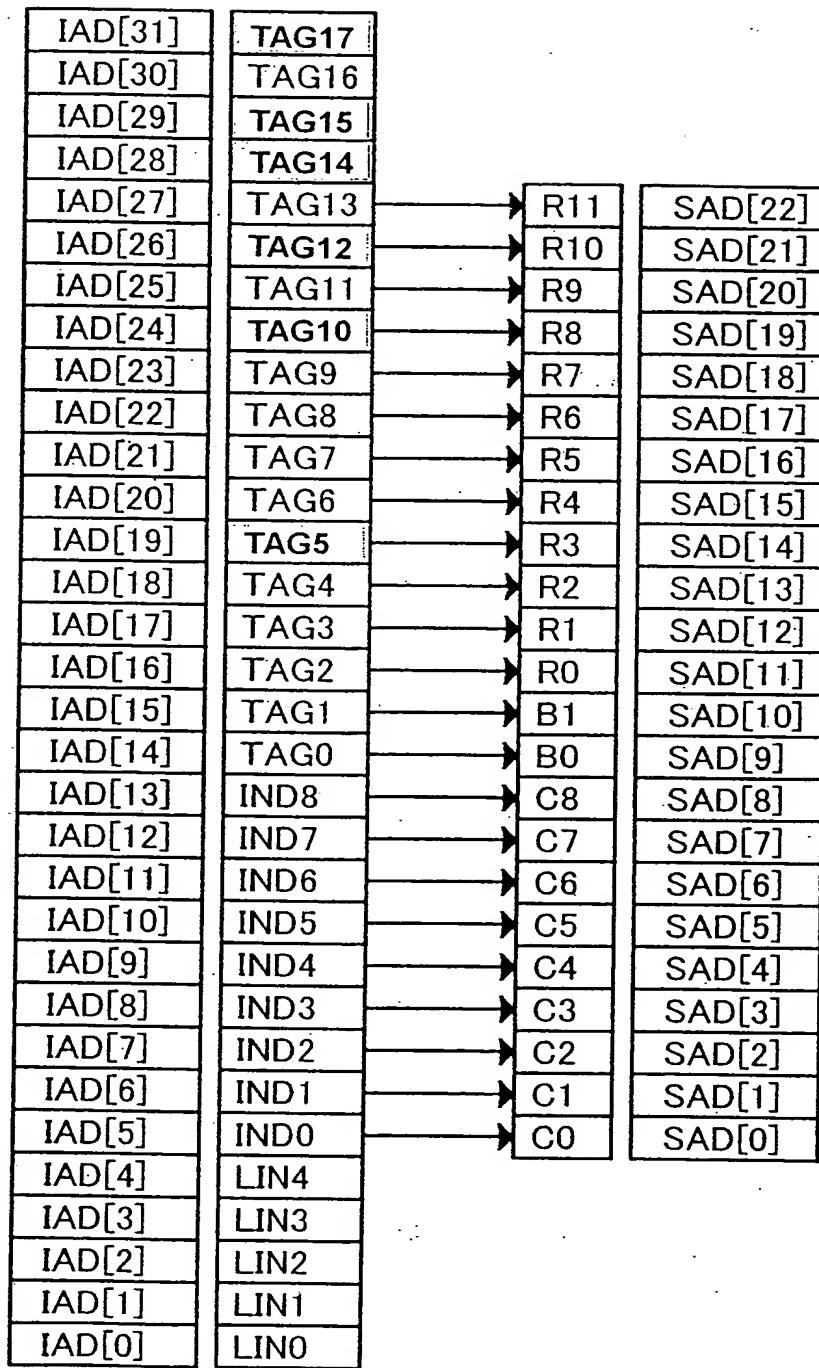
# FIG. 21



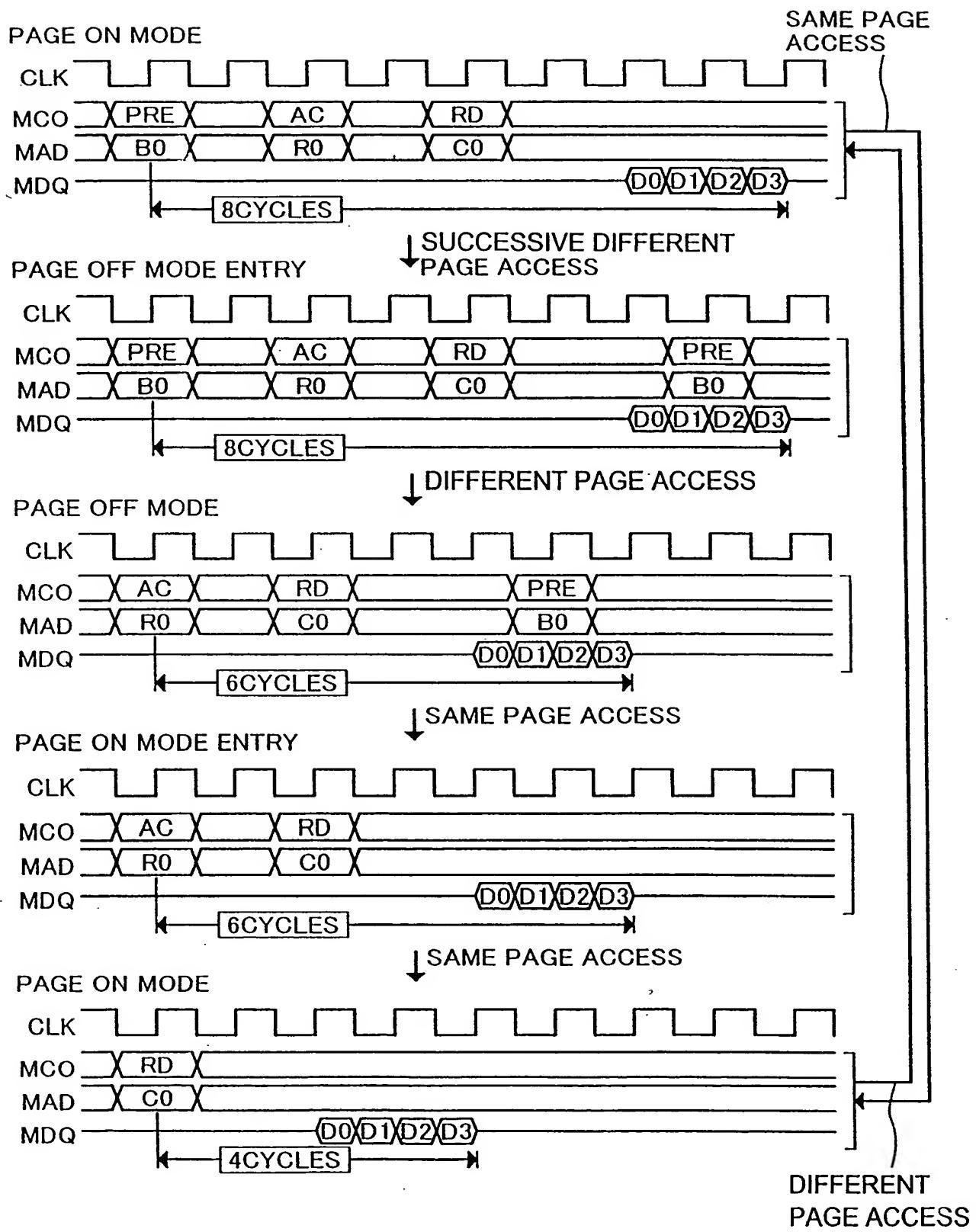
# FIG. 22



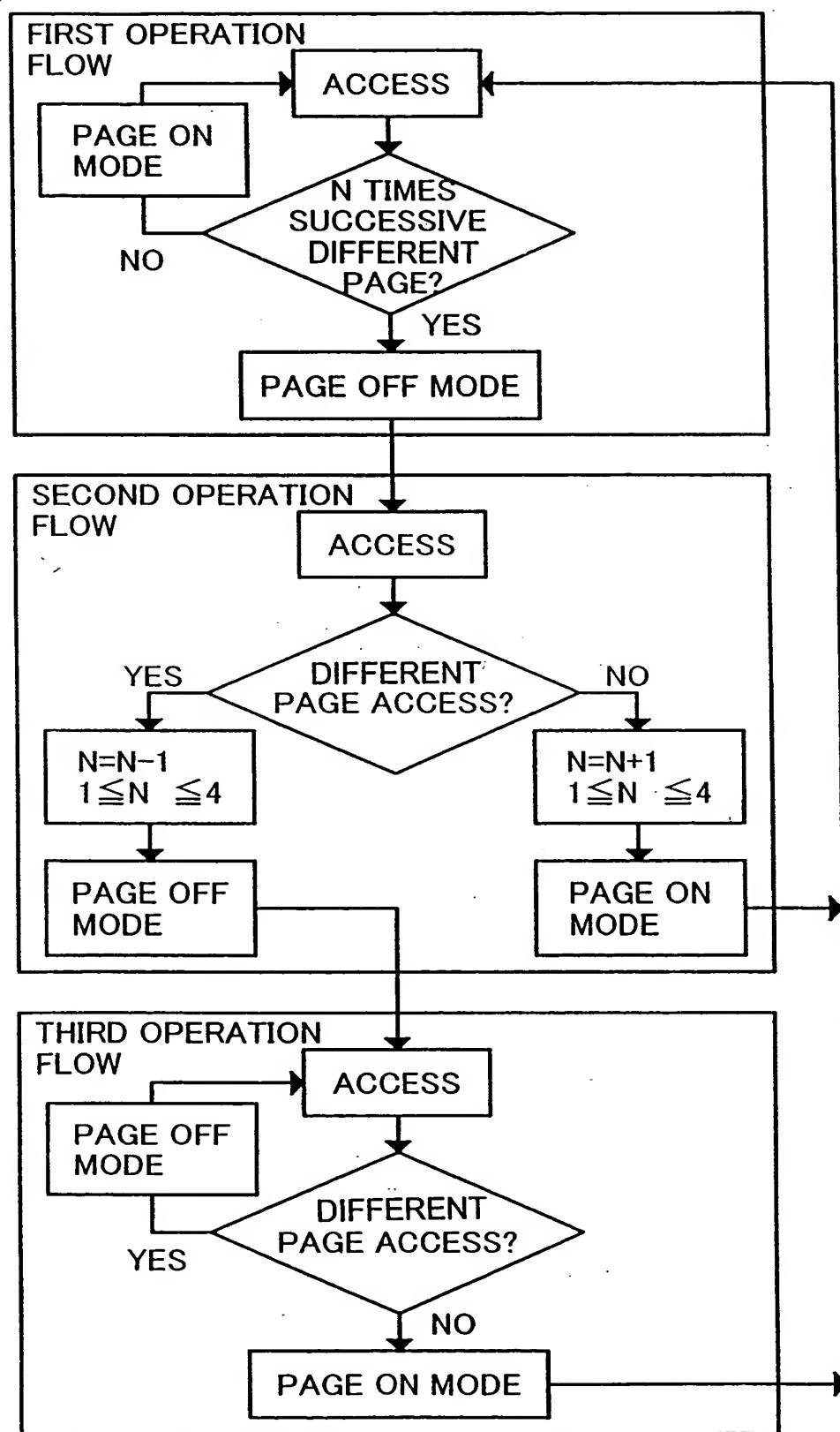
# FIG. 23



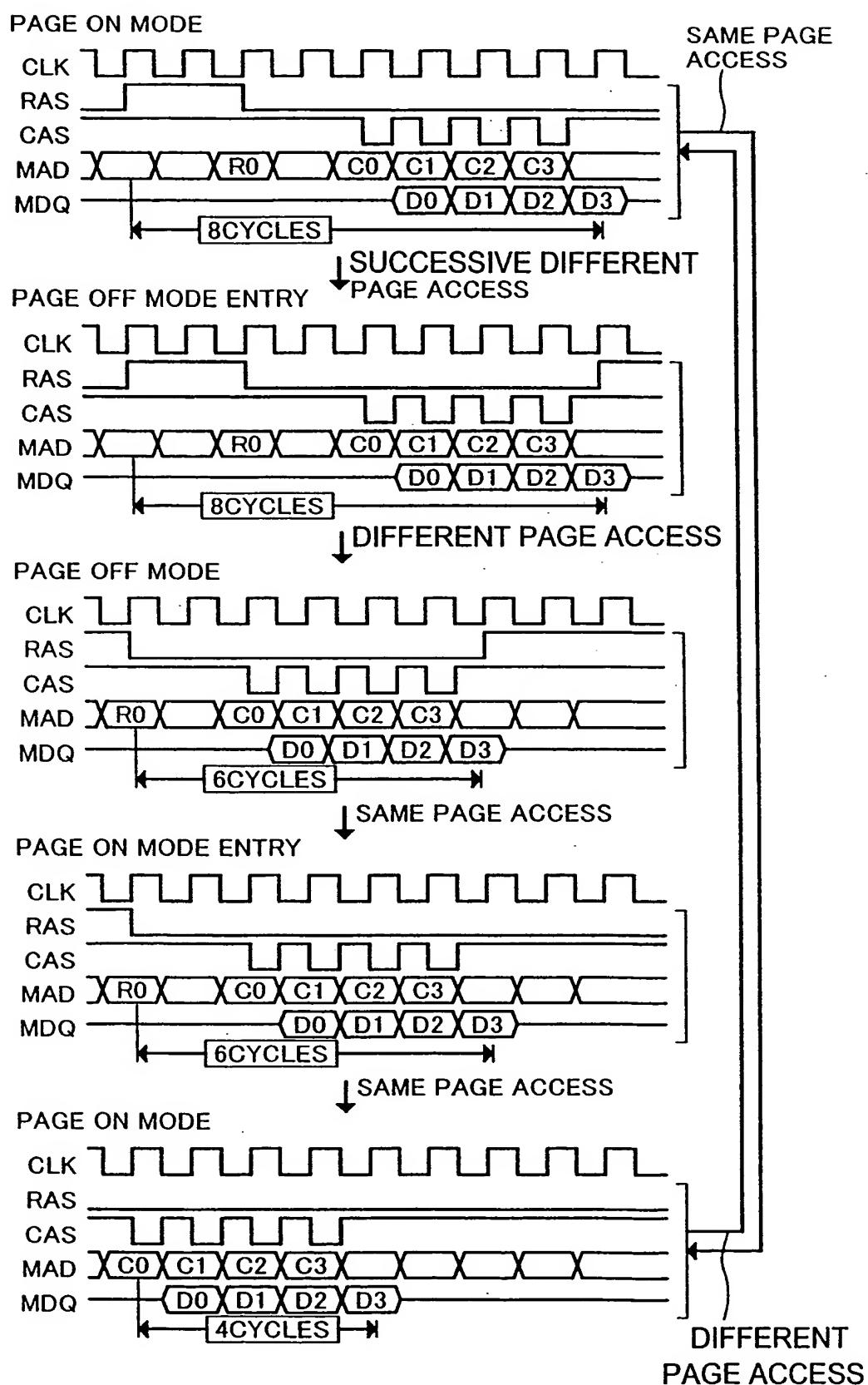
# FIG. 24



# FIG. 25



# FIG. 26



# FIG. 27

